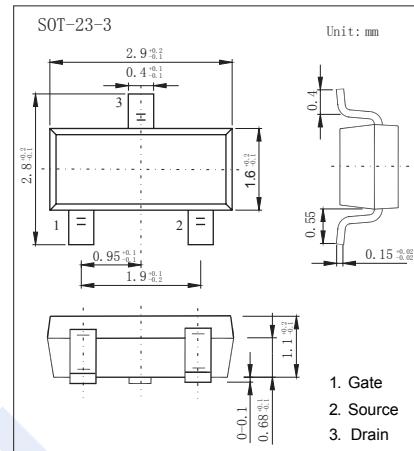
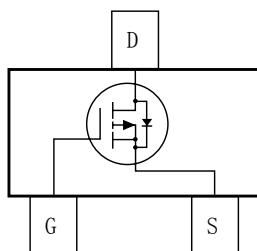


P-Channel MOSFET

FDN352AP (KDN352AP)

■ Features

- V_{DS} (V) = -30V
 - I_D = -1.3 A (V_{GS} = -10V)
 - $R_{DS(ON)} < 180\text{m}\Omega$ (V_{GS} = -10V)
 - $R_{DS(ON)} < 300\text{m}\Omega$ (V_{GS} = -4.5V)



■ Absolute Maximum Ratings $T_a = 25^\circ\text{C}$

Parameter	Symbol	Rating	Unit
Drain-Source Voltage	V _{DS}	-30	V
Gate-Source Voltage	V _{GС}	±25	
Continuous Drain Current	I _D	-1.3	A
Pulsed Drain Current	I _{DM}	-10	
Power Dissipation (Note.1) (Note.2)	P _D	0.5	W
		0.46	
Thermal Resistance.Junction- to-Ambient	R _{thJA}	250	°C/W
Thermal Resistance.Junction- to-Case	R _{thJC}	75	
Junction Temperature	T _J	150	°C
Junction Storage Temperature Range	T _{stg}	-55 to 150	

Note.1: $R_{\theta JA} = 250^{\circ}\text{C}/\text{W}$ when mounted on a 0.02 in² pad of 2oz. copper.

Note.2: $R_{\theta JA} = 270^{\circ}\text{C/W}$ when mounted on a 0.001 in² pad of 2oz. copper.

P-Channel MOSFET

FDN352AP (KDN352AP)

■ Electrical Characteristics $T_a = 25^\circ\text{C}$

Parameter	Symbol	Test Conditions	Min	Typ	Max	Unit
Drain-Source Breakdown Voltage	V_{DSS}	$I_D=-250 \mu\text{A}, V_{GS}=0\text{V}$	-30			V
Zero Gate Voltage Drain Current	$I_{DS(on)}$	$V_{DS}=-24\text{V}, V_{GS}=0\text{V}$			-1	μA
Gate-Body leakage current	I_{GSS}	$V_{DS}=0\text{V}, V_{GS}=\pm 25\text{V}$			± 100	nA
Gate Threshold Voltage	$V_{GS(th)}$	$V_{DS}=V_{GS}, I_D=-250 \mu\text{A}$ (Note.1)	-0.8		-2.5	V
Static Drain-Source On-Resistance	$R_{DS(on)}$	$V_{GS}=-10\text{V}, I_D=-1.3\text{A}$ (Note.1)			180	$\text{m}\Omega$
		$V_{GS}=-4.5\text{V}, I_D=-1.1\text{A}$ (Note.1)			300	
		$V_{GS}=-4.5\text{V}, I_D=-1.1\text{A}, T_J=125^\circ\text{C}$ (Note.1)			400	
Forward Transconductance	g_{FS}	$V_{DS}=-5\text{V}, I_D=-0.9\text{A}$		2		S
Input Capacitance	C_{iss}	$V_{GS}=0\text{V}, V_{DS}=-15\text{V}, f=1\text{MHz}$		150		pF
Output Capacitance	C_{oss}			40		
Reverse Transfer Capacitance	C_{rss}			20		
Total Gate Charge	Q_g	$V_{GS}=-4.5\text{V}, V_{DS}=-10\text{V}, I_D=-0.9\text{A}$ (Note.1)		1.4	1.9	nC
Gate Source Charge	Q_{gs}			0.5		
Gate Drain Charge	Q_{gd}			0.5		
Turn-On Delay Time	$t_{d(on)}$	$V_{GS}=-10\text{V}, V_{DS}=-10\text{V}, I_D=-1\text{A}, R_G=6\Omega$ (Note.1)			8	ns
Turn-On Rise Time	t_r				28	
Turn-Off Delay Time	$t_{d(off)}$				18	
Turn-Off Fall Time	t_f				2	
Body Diode Reverse Recovery Time	t_{rr}	$I_F=-3.9\text{A}, dI/dt=100\text{A}/\mu\text{s}$		17		nC
Body Diode Reverse Recovery Charge	Q_{rr}			7		
Maximum Body-Diode Continuous Current	I_S				-0.42	A
Diode Forward Voltage	V_{SD}	$I_S=-0.42\text{A}, V_{GS}=0\text{V}$ (Note.1)			-1.2	V

Note.1:Pulse Test: Pulse Width < 300 μs , Duty Cycle < 2.0%

■ Marking

Marking	52AP
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P-Channel MOSFET

FDN352AP (KDN352AP)

■ Typical Characteristics

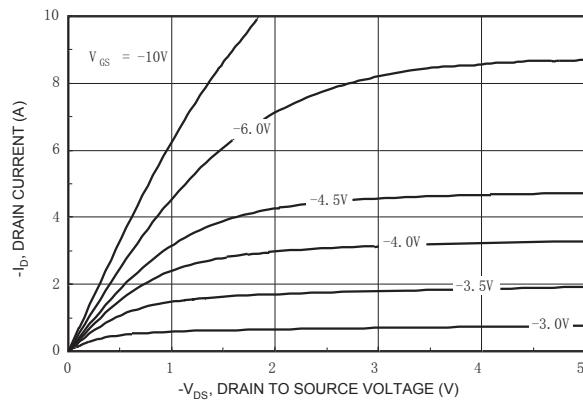


Figure 1. On-Region Characteristics.

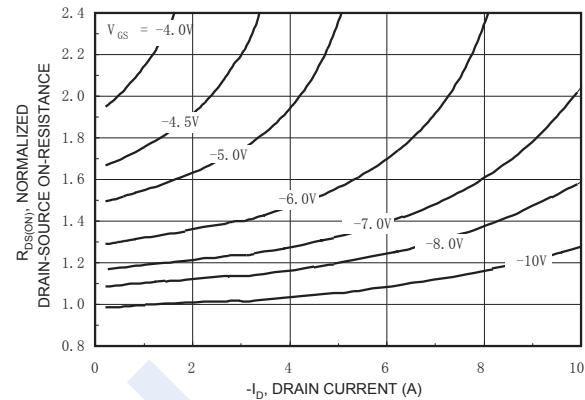


Figure 2. On-Resistance Variation with Drain Current and Gate Voltage.

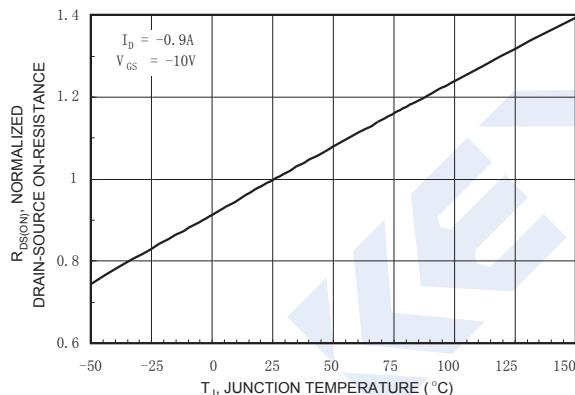


Figure 3. On-Resistance Variation with Temperature.

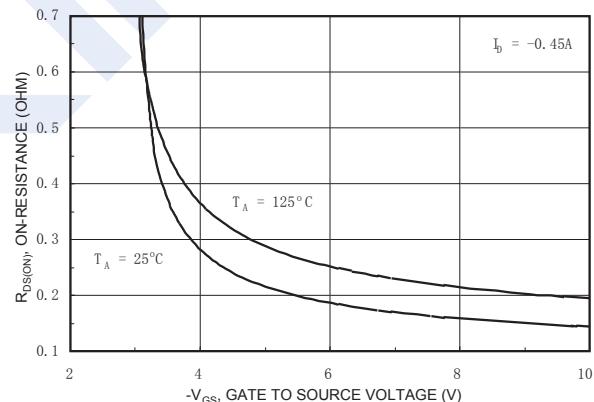


Figure 4. On-Resistance Variation with Gate-to-Source Voltage.

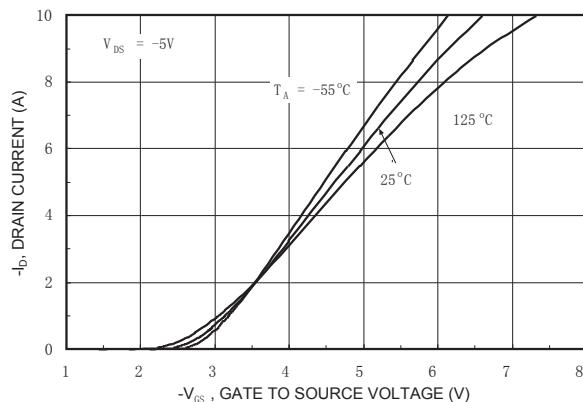


Figure 5. Transfer Characteristics.

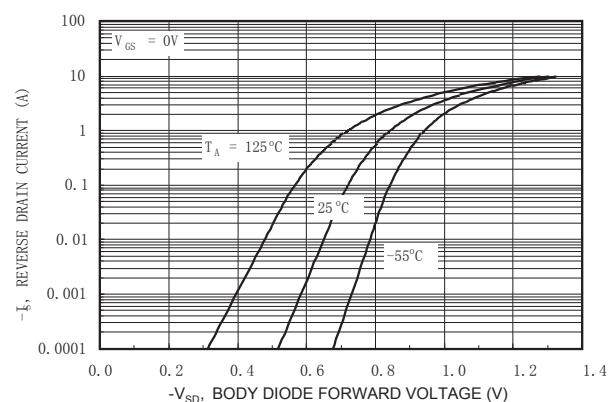


Figure 6. Body Diode Forward Voltage Variation with Source Current and Temperature.

P-Channel MOSFET

FDN352AP (KDN352AP)

■ Typical Characteristics

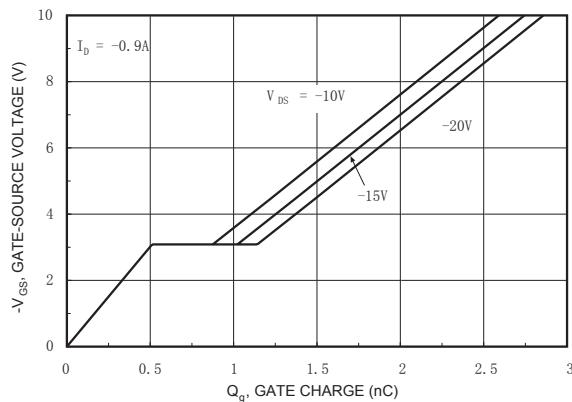


Figure 7. Gate Charge Characteristics.

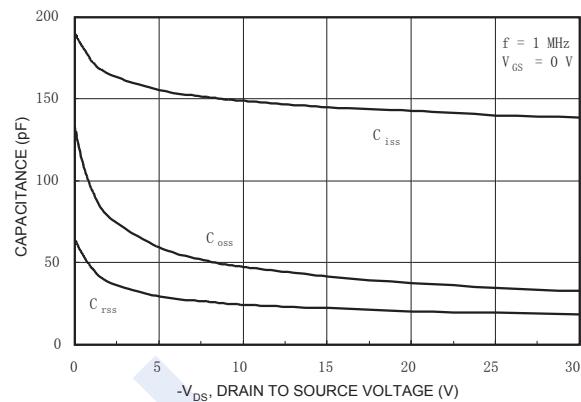


Figure 8. Capacitance Characteristics.

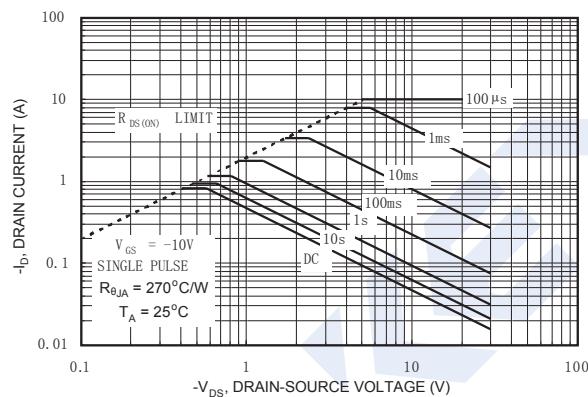


Figure 9. Maximum Safe Operating Area.

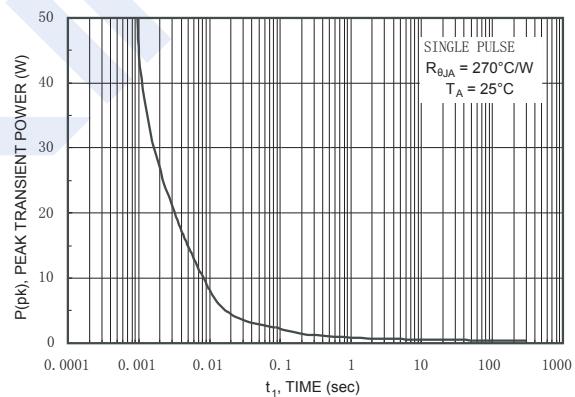


Figure 10. Single Pulse Maximum Power Dissipation.

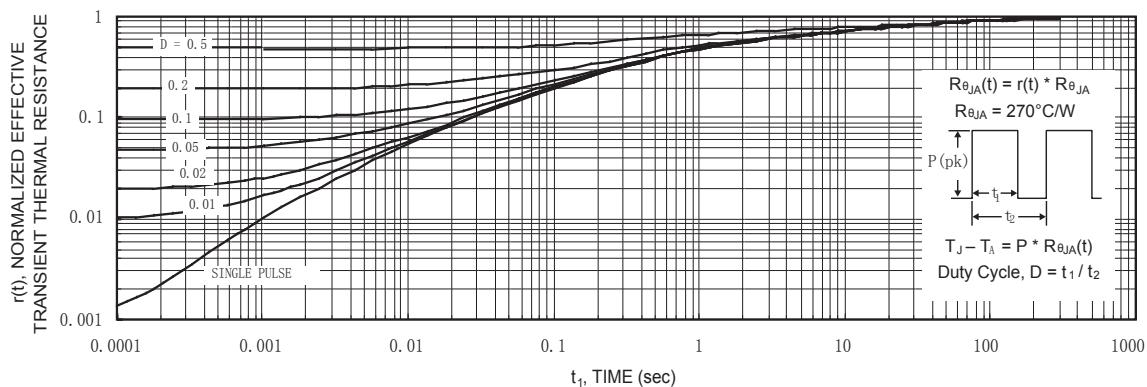


Figure 11. Transient Thermal Response Curve.